

### ABSTRACT

A DSL device performs a frame check sequence operation in a digital signal processor (DSP), *i.e.*, layer one of the OSI seven layer model, heretofore performed only in layer two of the OSI seven layer model. Performing the layer two framing and calculating the frame check sequence in the DSP allows the DSP to use the layer two error detection results, and optionally, the mean squared error vector, to more accurately determine whether to update adaptive receiver parameters.

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